



MS AF
REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2133

PATENT
1248-0509P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: MIYACHI, Kumi et al. Conf.: 3629
Appl. No.: 09/615,956 Group: 2133
Filed: July 13, 2000 Examiner: Esaw T. Abraham
For: SEMICONDUCTOR DEVICE HAVING
INTEGRALLY SEALED INTEGRATED CIRCUIT
CHIPS ARRANGED FOR IMPROVED TESTING

EA
OK to enter
07/19/04

REQUEST FOR RECONSIDERATION
AFTER FINAL UNDER 37 C.F.R. § 1.116

RECEIVED

JAN 29 2004

Technology Center 2100

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 26, 2004
first Monday

Sir:

In reply to the Office Action dated November 24, 2003, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This reply includes amendments and remarks.

E

- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☒ No fee is required.
- ☐ Check(s) in the amount of \$0.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By Robert C. Down # 48,222
For Charles Gorenstein, #29,271

CG/RWD:kss
1248-0509P

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Attachment(s)

(Rev. 09/30/03)

